

Iobase

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Contents

1 Iobase	3
1.1 Contributors	4
2 Contributors	5
List of Figures	7
3 Licenses	11
3.1 GNU GENERAL PUBLIC LICENSE	11
3.2 GNU Free Documentation License	12
3.3 GNU Lesser General Public License	13

1 Iobase

Obsolete - This page has obsolete information that is likely no longer relevant and will be deleted.

iobase is used potentially for several things, but currently it is only coded for installs on a x86 based PC.

This has no value on a Pi. (currently)

The setting goes in a node stanza for which it is used in rpt.conf.

iobase = (options)

The options are the bios address of a parallel port on a PC. These potential addresses are HEX addresses for the port.

378 (lpt1)

278 (lpt2)

3BC (lpt3)

Note that the addresses can extend beyond this but the address must match the physical hardware. Only standardized are shown. Also, depending on the age of a PC, the bios plug and play system can be unreliable if you can not manually define built-in device addresses and plug-n-play system shuffles addresses on the placement of new hardware.

The first use of them may be just for potential input/output sensing and switching (GPIO) You have 8 outputs (0-7) and 5 inputs.

These output pins can be assigned to PTT or with the aid of a switching transistor/relay turn on/off a fan or any other device.

The input pins can be assigned to COS or as a flag for hardware based CTCSS or any sensor you want to create a action for like site alarm or over-temperature sensor etc.

The first 3 output pins are devoted to a Doug Hall RBI-1 remote base interface 'if that has been selected'. So if you plan to use it for a RBI-1, you might want to start from output 4 (logical 3) for other assignments.

It is possible to have as many parallel ports as you can address, but only 'one per node' may be defined.

Also important to note that USB to parallel converters can not be 'bit-banged' to function on linux machines for the above purposes.

1.1 Contributors

- KB8JNM¹
- N8EI²
- This page was last edited on 24 October 2024, at 12:50.

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1 Chapter 3 on page 11

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